

Electronic Acknowledgement Receipt

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Application Number:	10597919
Confirmation Number:	7894
Title of Invention:	MULTILAYER SUBSTRATE WITH BUILT-IN CHIP-TYPE ELECTRONIC COMPONENT AND METHOD FOR MANUFACTURING THE SAME
First Named Inventor:	Osamu CHIKAGAWA
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Application Type:	U.S. National Stage under 35 USC 371
International Application Number:	PCT/JP05/19286

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
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1	Application Data Sheet	36856-1458-PTO-20060811-Application_Data_Sheet.pdf	1733849	no	4
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2	Miscellaneous Incoming Letter	36856-1458-PTO-20060811-Express_Request_to_Begin_National_Stage_Examination.pdf	58399	no	1
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3	Authorization for Extension of Time all replies	36856-1458-PTO-20060811-General_authorization.pdf	59074	no	1
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4		36856-1458-PTO-20060811-Preliminary_Amendment.pdf	87384	yes	8
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5		36856-1458-PTO-20060811-Specification_Original.pdf	129821	yes	31
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10	Information Disclosure Statement (IDS) Filed	36856-1458-PTO-20060811-PTO_SB_08.pdf	952086	no	5
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11	NPL Documents	36856-1458-Prior-Art-International_Search_Report.pdf	17118415	no	5
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12		36856-1458-PTO-20060811-References_submitted_with_IDS_new_app.pdf	8672177	yes	433
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

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